



**AUDISANKARA COLLEGE OF ENGINEERING & TECHNOLOGY
(AUTONOMOUS)**

Gudur, Nellore Dist - 524101, A.P (India)

Examination Branch

**Revised Timetable for M.Tech II Semester (R18) Regular & Supplementary
External Examinations October 2020**

Date/Day	Branch	Subject Code	Name of the Subject
05-11-2020 Thursday 10:00 AM to 01:00 PM	STE	18ST201	Finite Element Methods
	PE	18PE201	Flexible AC transmission Systems
	ES	18ES201	FPGA Architecture and Applications
	VLSI	18VL203	FPGA Architecture and Applications
	CSE	18CO201	Big Data Analytics
07-11-2020 Saturday 10:00 AM to 01:00 PM	STE	18ST202	Structural Dynamics
	PE	18PE202	Electric Drives-II
	ES	18ES202	Real Time Operating Systems
	VLSI	18VL202	Device Modeling
	CSE	18CO202	Mobile Application Development
10-11-2020 Tuesday 10:00 AM to 01:00 PM	STE	18ST203	Advanced Steel Design
	PE	18PE205	Energy Auditing, Conversion and Management
	ES	18ES204	Cryptography and Network Security
	VLSI	18VL201	Embedded System Concepts
	CSE	18CO205	Software Oriented Architecture
12-11-2020 Thursday 10:00 AM to 01:00 PM	STE	18ST206	Design of Advanced Concrete Structures
	PE	18PE208	Artificial Intelligence Computing Techniques and Applications
	ES	18ES206	Hardware Software Co-Design
	VLSI	18VL206	Hardware Software Co-Design
	CSE	18CO206	Internet of Things

Notification Date: 17-10-2020

Revised Notification Date: 26-10-2020

CONTROLLER OF EXAMINATIONS

DIRECTOR