



**AUDISANKARA COLLEGE OF ENGINEERING & TECHNOLOGY
(AUTONOMOUS)**

Gudur, Nellore Dist - 524101, A.P (India)

Examination Branch

**Revised Timetable for M.Tech I Semester (R18) Supplementary
External Examinations, October 2020**

Date/Day	Branch	Subject Code	Name of the Subject
06-11-2020 Friday 02:00 PM to 05:00 PM	STE	18ST101	Advanced Structural Analysis
	PE	18PE101	Modern Control Theory
	ES	18ES101	Microcontrollers for Embedded Systems Design
	VLSI	18VL101	CMOS Analog Integrated Circuit Design
	CSE	18CO101	Advanced Data Structures and Algorithms
09-11-2020 Monday 02:00 PM to 05:00 PM	STE	18ST102	Theory of Elasticity and Plasticity
	PE	18PE102	Electric Drives-I
	ES	18ES107	CMOS Digital Integrated Circuit Design
	VLSI	18VL102	CMOS Digital Integrated Circuit Design
	CSE	18CO102	UML and Design Patterns
11-11-2020 Wednesday 02:00 PM to 05:00 PM	STE	18ST104	Maintenance and Rehabilitation of Structures
	PE	18PE104	Power Electronic Converters
	ES	18ES103	VLSI Technology and Design
	VLSI	18VL104	VLSI Technology and Design
	CSE	18CO103	Cryptanalysis
13-11-2020 Friday 02:00 PM to 05:00 PM	STE	18ST107	Bridge Engineering
	PE	18PE108	Digital Control Systems
	ES	18ES102	Embedded System Concepts
	VLSI	18VL108	Algorithms for VLSI Design Automation
	CSE	18CO106	Cloud Computing
17-11-2020 Tuesday 02:00 PM to 05:00 PM	STE	18AS101	Research Methodology and IPR
	PE		
	ES		
	VLSI		
	CSE		

Notification Date: 17-10-2020

Revised Notification Date: 26-10-2020

CONTROLLER OF EXAMINATIONS

DIRECTOR